

## HIGH EFFICIENCY POWER AMPLIFIER SYSTEMS AND METHODS

### Cross Reference to Related Application

[0001] This application claims the benefit of U.S. provisional application  
5 serial No. 60/243,729 filed on 30 October, 2000 entitled "High Efficiency  
Multi-Carrier Microwave Power Amplifier Subsystem".

### Technical Field

[0002] This invention relates to telecommunication and power  
10 amplification. The invention relates in particular to radio frequency power  
amplification and to power amplifier systems suitable for use in  
telecommunication applications such as cellular telephone repeaters.  
Embodiments of the invention provide amplification systems having band width  
sufficient for multi-channel operation.

### Background

[0003] Power amplifiers of the type used in telecommunication  
applications are often inefficient. It is not unusual for such amplifiers to have  
energy efficiencies of well under 50%. This causes problems for the providers  
20 of telecommunication services because it is often desirable to provide power  
amplifiers in remote locations, such as hilltops. Delivering electrical power to  
such remote locations can be exceedingly expensive. Dealing with excess heat  
given off by inefficient power amplifiers can also be expensive and  
inconvenient.

25 [0004] Some designs of power amplifier are known to be more efficient  
than others. However, not all types of amplifier are well adapted to  
telecommunication or radar applications. Such applications typically require

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amplification systems which meet specifications relating to linearity, bandwidth and/or adaptability.

[0005] Switching mode power amplifiers (also known as class S amplifiers) are known to be capable of high efficiencies. The class S amplifier has an idealized efficiency of 100%. Class S amplifiers have mainly been used in lower frequency applications (typically up to a few kHz). To use a class S amplifier at higher frequencies requires switching devices capable of switching state very fast.

[0006] The prior art describes various power amplification systems, some of which promise increased efficiency. These include the systems described in U.S. patent Nos. 4,717,884; 5,159,287; and 5,905,407.

[0007] The characteristics of an amplification system depend not only upon the design of the amplifier itself but also depend upon the way in which the amplified signal is modulated. The so-called delta-sigma modulator has been studied extensively and has some desirable properties. Specific embodiments of the delta-sigma modulator are described, in United States patent Nos. 5,446,460 and, 5,714,916. Delta-sigma modulators are analysed and discussed in S.R. Norsworthy et al., *Delta Sigma data Converters: Theory, Design and Simulation*, IEEE Press, New York, 1997; James Cherry and W. Martin Snelgrove, *Continuous Time Delta Sigma Modulators for High speed A/D Conversion*, Kluwer Academic Publishers, Boston, 2000; and, Arun Jamayaraman et al., *Linear high-efficiency Microwave Power amplifiers using*

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*Band-pass Delta Sigma Modulators*, IEEE Microwave and Guided Letters Vol. 2 No.3 March 1998, each of which is hereby incorporated by reference.

[0008] Despite the range of power amplification options provided by the prior art there remains a pressing need for power amplification systems which offer increased efficiency. There is a particular need for such systems which can be practically used for telecommunication applications, especially in remote locations.

#### Brief Description of the Drawings

[0009] In drawings which illustrate non-limiting embodiments of the invention:

Figure 1 is a block diagram of an amplification system ;

Figure 2 is a schematic diagram illustrating a class S amplifier having a totem pole configuration;

Figure 3 is a schematic diagram illustrating a class S amplifier having an "H" configuration;

Figure 4 is a schematic diagram illustrating a bandpass filter / matching network;

Figure 5 is a block diagram of a bandpass delta-sigma modulator;

Figure 6 is a model of the bandpass delta-sigma modulator of Figure 5;

Figure 7A is a block diagram of a microwave repeater system including an extended interface; and,

Figure 7B is a block diagram of a microwave repeater system including an extended interface and a plurality of amplifiers.

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### Detailed Description

[0010] Throughout the following description, specific details are set forth in order to provide a more thorough understanding of the invention. However, the invention may be practised without these particulars. In other instances, well known elements have not been shown or described in detail to avoid unnecessarily obscuring the invention. Accordingly, the specification and drawings are to be regarded in an illustrative, rather than a restrictive, sense.

[0011] One aspect of this invention provides an amplification system which comprises the combination of an efficient power amplifier, a delta-sigma modulator and a linearizer circuit. While the invention is described herein primarily with reference to an embodiment useful for a microwave telecommunication repeater station the invention is not limited to this application. The amplification system may be used in base stations and in portable devices, such as cellular telephone handsets or other mobile radios. The amplification system may be applied in other contexts where amplification of signals is required. Preferred embodiments comprise an extended interface as described below. Some embodiments are powered by alternative power sources such as an array of solar cells, an electrical storage device, such as a storage battery powered by solar cells, wind power, or another source of energy which is available in the vicinity of the power amplifier.

[0012] Figure 1 is a block diagram of an amplification system 10 according to the invention. Amplification system 10 comprises a modulator 12 which receives a signal 13 to be amplified at its input 14. Modulator 12 produces a digital output signal. Modulator 12 may be a delta sigma

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modulator. Modulator 12 has an output 16 coupled to an input 18 of a high efficiency amplifier 20. Amplifier 20 may be a class S amplifier. An output 22 of amplifier 20 is coupled to an antenna system 24. Preferred embodiments include a linearizer 30. Linearizer 30 is coupled to receive signal 13 at input 14 and/or the signal 19 at output 22 and to develop a correction signal which is combined with the signal at a point before it is received at the input 18 of amplifier 20.

[0013] The overall characteristics of amplification system 10 are improved by linearizer 30. Linearizer 30 may implement any of a range of suitable linearization schemes. The functions of such linearization schemes include one or more of reducing distortion, reducing spectral re-growth, maintaining narrow frequency spacing between carriers and optimizing bandwidth. Linearizer 30 may provide linearization by one of or a combination of

- Feed Forward;
- Analog Predistortion;
- Digital Predistortion;
- Adaptive Digital Predistortion;
- Predistortion and Feed Forward; and,
- Adaptive Feedforward.

[0014] Modulator 12 may comprise a delta-sigma modulator, which may be a band-pass delta-sigma modulator (BPDSM) of  $N^{\text{th}}$  order (typically  $4^{\text{th}}$  order) with cascaded stages of 1 bit or multi-bit analog to digital converters (ADCs) and digital to analog converters (DACs). Modulator 12 converts an

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rectangular wave output signal 17. Signal 17 drives amplifier 20. Input RF signal 13 has a frequency in excess of about 300 kHz and may be a microwave frequency signal (i.e. signal 13 may have a frequency of about 800MHz or more).

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[0015] In some currently preferred embodiments of the invention, amplifier 20 comprises a switching mode power amplifier. For example, amplifier 20 may comprise a class S amplifier. Amplifier 20 could instead comprise a Class D switching-mode power amplifier. In other embodiments within the scope of the invention amplifier 20 may be of other types such as class C, E or F amplifier types. Especially for low power applications amplifier 20 may comprise an integrated circuit which carries electronic components which constitute amplifier 20. The integrated circuit may be fabricated using Miniature Microwave Integrated Circuit (MMIC) technology.

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[0016] A switching mode power amplifier ("SMPA") includes active devices such as transistors or other electronic switches. Such amplifiers can be more efficient than an amplifier in which the active devices serve as current sources. An ideal switch has either zero voltage across its terminals or zero current through it at all times and therefore, no power is dissipated in the device. Where a transistor or other active device is used as a switch, the transistor or other active device may be configured so that it is working either in a pinch-off region or in saturation to approach the characteristics of an ideal switch.

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[0017] In switching mode power amplifiers of class S or class D, amplification is achieved typically by two or more active devices. The devices typically comprise power transistors which may be connected in one of several configurations. Figures 2 and 3 illustrate simple class S amplifier configurations which are examples of configurations that may be used in this invention. A great many other amplifier configurations are known to those skilled in the art. The amplification circuits of Figures 2 and 3, together with all other suitable amplifier circuits known in the art have the function of amplifying an input signal and may be generally termed "means for amplifying".

[0018] Figure 2 shows a simple class S amplifier 20A. Amplifier 20A has a pair of power transistors Q1 and Q2 arranged in a totem-pole configuration. Transistors Q1 and Q2 operate as switches controlled by signals 17A and 17B at inputs 18A and 18B which together constitute an input 18 of amplifier 20A. Transistor Q2 is connected between output 22 and ground. Transistor Q1 is connected between an output 22 and a direct current power supply 40. Power supply 40 provides electrical power at a suitable voltage which may be, for example, in the range of 6V to 60 V.

[0019] The signals provided at inputs 18A and 18B cause transistors Q1 and Q2 to alternately connect output 22 to ground and to power supply 40. Transistors Q1 and Q2 are shunted by diodes D1 and D2. Diodes D1 and D2 protect transistors Q1 and Q2 respectively against damage due to switching transients. Transistors Q1 and Q2 may comprise field effect transistors or other signal controllable switching devices capable of operating as switches at the

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frequency of signals 17A and 17B. The signal controllable switching devices have the function of switching between electrically conducting and non-conducting states in response to a control signal (which may be electrical or optical). Such devices may be termed "means for switching".

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[0020] Antenna 24 is connected to output 22 of amplifier 20 by a dc blocking device, such as a capacitor 42 and a filter 43. In the illustrated embodiment filter 43 comprises an inductor 44 conducted in series between dc blocking capacitor 42 and antenna 24 and a capacitor 45 connected across antenna 24.

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[0021] A class S amplifier can be designed in a push-pull configuration with very wideband baluns providing a suitable phase inversion and impedance matching/conversion. All of the source terminals of switching devices in the amplifier may be grounded in this configuration.

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[0022] Figure 3 shows a class S amplifier 20B having switching devices arranged in an "H" configuration. The illustrated amplifier 20B comprises two pairs of power transistors (although other means for switching could also be used). Transistors Q1 and Q2 are connected in series with one another.

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Transistor Q1 is connected between an output 22A and a power supply 40. Transistor Q2 is connected between output 22A and ground. Transistors Q3 and Q4 are connected in series with one another. Transistor Q3 is connected between an output 22B and power supply 40. Transistor Q4 is connected between output 22B and ground.

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[0023] Signal 17A is provided at the control inputs of transistors Q1 and Q4. Signal 17B is provided at the control inputs of transistors Q2 and Q3. Signal 17B may be an inverted version of signal 17A. Signals 17A and 17B are both bi-level rectangular signals.

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[0024] When signal 17A has the proper polarity and amplitude, transistors Q1 and Q4 are conducting. This allows drain current (close to saturation) to flow at outputs 22A and 22B. While this occurs, signal 17B causes transistor Q2 and Q3 to be in an off state. The off state may be a pinch-off condition (i.e. no current flows through transistors Q2 and Q3 during this phase). When signals 17A and 17B reverse their levels, transistors Q1 and Q4 are switched to an off state (no drain current flowing) while transistors Q2 and Q3 are switched to an "on" state in which they permit current flow. Transistors Q2 and Q3 may be operating in saturation while in their on states.

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[0025] One of diodes D1 through D4 are connected in parallel with each of transistors Q1 through Q4. Diodes D1 through D4 protect transistors Q1 through Q4 respectively against damage due to switching transients. Such transients may occur as a result of reverse current flow from spikes generated by charges accumulated in a capacitor in an output filter. A driven load, such as an antenna or an antenna together with any desirable filtering circuits may be coupled between outputs 22A and 22B.

[0026] Amplifier 20 may comprise a plurality of band-specific amplification components operating in parallel. Each component may comprise one or more band-specific devices or circuits.

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[0027] The output of amplifier 20 may be coupled to an antenna by way of an output bandpass filter/matching network arrangement. Figure 4 shows an example of a possible bandpass filter / matching network 50. Bandpass filter 50 comprises a capacitor 60 and an inductor 62 coupled in parallel with an antenna 24. A series connected inductor, 64A, and capacitor 65A couple one antenna connection to the output of amplifier 20. A series connected inductor, 64B, and capacitor 65B couple the other antenna connection either to ground in the case of a single-sided amplifier as depicted, for example, in Figure 2 or to another amplifier output, as in the case of a double-sided amplifier as depicted, for example, in Figure 3.

[0028] Bandpass filter/matching network 50 may be coupled directly to the output of amplifier 20 (for example, to terminals 22A and 22B) or coupled by way of a signal conditioning circuit, such as a filter or the like, a monitoring circuit etc. Bandpass filter matching network 50 may provide the functions of transforming amplified digital signals into analog signals, reducing the harmonics content present in the signal at the output of amplifier 20, and/or providing a proper impedance transition between amplifier 20 and a load (e.g., antenna 24 or another termination). These functions may be combined, as disclosed, or provided by separate elements or groups of elements. Those skilled in the art are aware of many specific suitable circuit constructions capable of providing these functions.

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[0029] Network 50 may act as a tank circuit, converting the amplified rectangular shaped signals into an amplified version of the original analog signal 13.

5 [0030] The amplified signal at the output of amplifier 20 is ultimately manifested in antenna 24 as an analog signal containing the characteristics of the original input signal with an acceptably small level of spurious emissions. The amplified output signal is made available to an external load, such as an antenna, via suitable coaxial cables or waveguides.

[0031] Figures 5 and 6 show one example of a 4<sup>th</sup> order bandpass delta sigma modulator 12 that may be used in the context of the invention. Delta sigma modulator 12 comprises an analog to digital converter 70 which samples an analog input signal  $x(t)$  after it has passed through a pair of resonator circuits 71A and 71B. Analog to digital converter 70 samples input signal  $x(t)$  at a frequency which may be in excess of three times the frequency of input signal  $x(t)$  and could be, for example, 4 to 6 times greater than a frequency of input signal  $x(t)$ . The output  $y(nT)$  of the analog to digital converter 70 provides an output signal for delta sigma modulator 12. Analog to digital converter 70 may comprise a sampler and a quantizer. The quantizer may comprise a two-level (single bit) trigger which assigns logical "TRUE" level to a positive input and a logical "FALSE" level to a negative input.

25 [0032] Output signal  $y(nT)$  is converted to an analog signal by a digital to analog converter 74. Digital to analog converter 74 may operate at the same frequency as analog to digital converter 70. Digital to analog converter 74 is

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located in a feedback loop of modulator 12. Digital to analog converter 74 converts the sequence of discrete pulses of output signal  $y(nT)$  back to analog.

[0033] Bandpass delta sigma modulator 12 may have a double loop configuration as shown in Figure 4. The analog output from digital to analog converter 74 is combined (and is typically subtractively combined) with the input signal  $x(t)$  at points 75A and 75B. This configuration provides two overlapping feedback loops.

[0034] The quantization performed by analog to digital converter 70 can be modelled as shown in Figure 6 as the addition of a noise signal  $e(n)$  to the input signal  $x(n)$  received by analog to digital converter 70. If the noise signal is independent enough from the input signal then it can be considered as an independent disturbance or additive noise. The final output waveform based on superposition of the input signal and the noise signal can be expressed as the sum of terms related to signal and noise respectively as follows, for example.

$$Y(z) = \frac{H(z)}{1 + H(z)} X(z) + \frac{1}{1 + H(z)} E(z) = STF(z) \times X(z) + NTF(z) \times E(z) \quad (1)$$

Where  $Y(z)$ ,  $X(z)$  and  $Z(z)$  are Z- transforms of  $u(n)$ ,  $y(n)$  and  $e(n)$  respectively.

[0035] In the preceding expression,  $STF(z)$  may be called a signal transfer function and  $NTF(z)$  may be called a noise transfer function. At the poles of  $H(z)$  and for many frequencies where  $H(z)$  is much greater than one,  $NTF(z)$  is small. At such frequencies the quantization noise is reduced. A function of modulator 12 is to convert an input analog signal into a signal of a type suitable

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for driving amplifier 20. Where amplifier 20 comprises a switching mode power amplifier then the output of modulator 12 may comprise a two-level signal of amplitude sufficient to drive amplifier 20 at frequencies of interest. For specific applications, one or more preamplifier or translation level stages  
5 may be provided to boost the amplitude of the modulated signal to a level suitable to drive amplifier 20.

[0036] The output signal  $y(nT)$  can be a pulse density modulated signal. As shown in Figure 6, such an output from delta sigma modulator 12 is a binary digital signal that can be considered as the sum of an input signal and associated quantization noise. Modulator 12 is provided with digital filters to shape the output noise so that its spectrum has a valley corresponding to the input spectrum falls in. Out-of-band noise can be then removed using properly designed bandpass filters to produce an approximate replica of the original signal.  
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[0037] An extended interface 80, such as an optical interface, may be provided to couple the output signal from delta sigma modulator 12 to the input of amplifier 20. Delta sigma modulator 12 and amplifier 20 may be in widely  
20 separated locations. For example, delta sigma modulator 12 may be located in a location where a mains power supply is available while amplifier 20 may be located at a mountain top relay station. The extended interface may, for example, comprise an optical fiber data transmission line.

[0038] Figure 7A illustrates an amplification system 79 which includes an extended interface 80. In the illustrated embodiment, interface 80 comprises a  
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wideband two way interface. Interface 80 comprises a forward direction extended link 81A which carries data from a control location 82 to a transmitting location 84 and a reverse direction link 81B which carries data in the opposite direction. Each of links 81A and 81B may comprise a fiber optic signal conductor 86, an electrical to optical transducer 87 and an optical to electrical transducer 88. A single optical fibre may be used to combine both forward and reverse direction links 81A and 81B by using optical wavelength division multiplexing. This may be implemented, for example, by providing electro-optical converters with different optical wavelengths in each direction along with appropriate optical filters. The filters could be, for example, 1.3  $\mu\text{m}$  and 1.5  $\mu\text{m}$ .

[0039] Extended interface 80 could comprise one or more low-loss RF coaxial cables, or other RF waveguides in place of a fiber optic cable. In some cases, multiple channels may be carried on a single waveguide through the use of a suitable number of frequency diplexers. The diplexers direct the selected receiver or transmitter signals according to their center frequencies and bandwidths. Extended interface 80 could comprise signal carriers of a cable TV distribution system or a microwave radio relay system. The optical fiber, coaxial cable, other waveguide, or radio relay system, provides the function of carrying signals between control location 82 and transmitter location 84.

[0040] A modulator 12 which produces a digital output signal is located at control location 82. The digital output signal is carried to transmitting location 84 on forward direction link 81A. The digital output signal is received at transmitting location 84 and applied as an input to amplifier 20. Amplifier 20

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amplifies the digital signal and produces an amplified output signal. The amplified output signal is coupled to an input of a bandpass filter which may be a programmable bandpass filter. A signal output from the bandpass filter is coupled to a load such as an antenna. Extended interface 80 may be long, for example from the bottom to the top of a mountain, or short. A long extended interface 80 may span more than more than 100 meters, more than 500 meter, or more than 1000 meters.

[0041] Amplifier 20 may be located very close to antenna 24. For example, where antenna 24 is on a tower, amplifier 20 may also be on the tower at the location of antenna 24, possibly immediately behind antenna 24 or proximate to the connections to antenna 24. Modulator 12 may be located in another location such as at a control location at the bottom of the tower or a control location completely away from the tower. In this disclosure the term "tower" means a structure providing an elevated location for mounting an antenna and includes purpose built tower structures, other man-made structures, and suitable natural features either in their natural states or modified by man.

[0042] The signal to be transmitted at transmitting location 84 may be an amplified version of a signal received at a receiver located at or in the vicinity of transmitting location 84. In this case, input signal 13 may be carried to control location 82 by way of reverse direction link 81B. Input signal 13 may be digitized by an analog-to-digital converter for transmission on reverse direction link 81B. Input signal 13 may be multiplexed with other data such as amplifier power monitoring data and other telemetry signals. Where multiple channels are to be included in a single input signal 13, an N:1 combiner 85 may

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combine the channel signals to provide signal 13 which is modulated by modulator 12.

[0043] The signal may be processed in various ways at control location 82 and then carried back to transmitting location 84 by way of forward direction link 81A to transmitting location 84 for amplification and transmission. The processing may include digital filtering.

[0044] Reverse-direction link 81B may carry feedback signals to control location 82. The feedback signals are received at location 82 where they can be used as an input to a linearizer 30 (not shown in Figure 7A). A linearizer maybe implemented in DSP 98, processor 89, or as a separate system.

[0045] One or more telemetry channels may be provided by extended interface 80. In the illustrated embodiment, this is done by providing a multiplexer 90 and a demultiplexer 91 at first and second ends respectively of links 81A and 81B. Telemetry channels of links 81A and 81B may carry power monitoring signals, control signals, signals regarding the operational status of subsystems at transmitting location 84, alarm signals and the like.

[0046] In the illustrated embodiment, the power output of amplifier 20 is monitored in real time by a power monitor 92. Power monitor 92 may comprise an RF power detector such as an RF Root Mean Square (RF RMS) composite power monitor circuit which generates an output signal 93. Output signal 93 is carried to control location 82 by a telemetry channel on receive direction link 81B. Power monitor 92 is coupled to the input and/or output of power amplifier



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20 by way of suitable directional couplers and /or attenuators. Power monitor  
92 monitors the voltage at the output 22 of power amplifier 20 for the required  
frequency and level ranges. This may be done with a suitable analog to digital  
converter. RMS power may be determined by a microcontroller 95 which  
5 receives information regarding voltages sampled by the analog to digital  
converter. Power monitor 92 can be calibrated against frequency, and level  
references and temperature compensated in ways known to those skilled in the  
art of RF power monitoring circuits.

[0047] In the illustrated embodiment, power monitor output signal 93 is  
provided to linearizer 30. Linearizer 30 may generate its corrective signal  
based, at least in part, on signal 93. For example, linearizer 30 may perform  
predistortion or other linearization enhancements based on signal 93.

[0048] The power output of amplifier 20 may be dynamically controlled  
to provide adequate power in regard to current propagation and traffic loading  
conditions. For example, when channels of a multichannel carrier are  
unoccupied, transmitter power may be reduced. As the signal attenuation  
changes on a propagation path between antenna 24 and a receiver, the power  
15 can be adjusted to maintain a desired signal to noise performance. The receiver  
may be moving relative to antenna 24. When the receiver is near antenna 24 in  
a line of sight then the power of amplifier 20 may be reduced. When the  
receiver is farther from antenna 24 or not in a clear line of sight then the power  
output of amplifier 20 may need to be increased.

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[0049] The power output of amplifier 20 may be adjusted in response to factors which may include one or more of channel count (the number of active, and/or overhead channels), channel link loss parameters, parameters specified by an air link protocol, such as IS95A,B or the like. The parameters which affect the desirable power output may be maintained as data accessible to microcontroller 95. Microcontroller 95 sets the output power by way of a power control interface 96.

[0050] To permit the output power to be controlled on demand, amplifier 20 may be configured to permit:

- Actively biasing the power devices by adjusting the drain current and/or voltage, according to demand, and/or
- Enabling or disabling devices in a multiple device power-combining design.

[0051] A multiple device power-combining design may comprise, for example, multiple paralleled H configurations. A number of class-S amplifiers in parallel may be provided and electronically selected to increase or decrease the radiated power on a specific antenna.

[0052] Spectrum performance may be monitored by a monitoring system 94. Spectrum monitoring system 94 may comprise a separate system or may comprise suitable software running on a processor 95 which receives a signal modulated by a microwave delta sigma modulator (which may also serve as a part of power monitoring system 92). An output signal from spectrum monitoring system 94 may be carried back to control location 82 on receive

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direction link 81B. A decimating filter maybe provided to down-convert the microwave signal to baseband for digital signal processing. A digital signal processor 98 analyzes the signal output by amplifier 20. Processor 96 may monitor for in-band and out-of-band spurious emissions, including intermodulation products.

[0053] Short, open or impairment conditions on a transmission line or antenna 24 connected to the output of amplifier 20 may be detected with a line integrity monitor (LIM). The LIM comprises an RF power and spectrum monitoring system 94 and associated processor 95. The LIM is switched between the forward and reverse port of a bi-directional coupler 97 for measurements. The LIM periodically determines a return loss versus distance profile and monitors antenna intermodulation performance based on detected forward and reverse signals. Alarms are generated and transmitted to control location 82 when the return loss versus distance profile and/or antenna intermodulation performance satisfy an alarm criterion. The alarm criterion may comprise a comparison of one or more of these values to one or more preset thresholds.

[0054] Figure 7B illustrates a transmitting system 99 which includes multiple amplifiers 20 each of which drives a separate antenna 24. The antennas 24 may comprise independent antennas or sections in a multisector antenna. System 99 provides multiple channels. Each channel provides a signal feed to an antenna 24. System 99 typically provides one antenna 24 per sector of a multi-sector antenna. The multi-sector antenna may, for example, have 6 sectors. The multi-sector antenna may be on a tower with amplifiers 20

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installed on the tower to operate simultaneously with several fixed sector antennas.

[0055] Antennas 24 may be arranged to provide a phased array. The array may comprise a number  $N$  of elements having a first orientation, for example vertical, and a number  $M$  of elements having a second orientation, for example horizontal. Such an array may require  $N \times M$  channels with one channel providing a suitably phased signal a corresponding element of the array. The amplifiers 20 driving the array elements may be electronically controlled to provide beam steering. The system may comprise one or more arrayed antennas and/or multi-sectored antennas on the same tower with a plurality of amplifiers receiving modulation signals over the same extended interface from modulators located at another (second) end of the extended interface.

[0056] A four port directional coupler may be provided at the output of amplifier 20 with power and spectrum monitoring capabilities. This permits measurements on forward and reverse directions of the antenna transmission line to derive Return Loss (RL) and distance to fault values. Alarm thresholds may be set for these parameters and exceptions reported over the telemetry link.

[0057] In the embodiment of Figure 7B, extended interface 80 comprises a single fiber optic link wherein signals for the different channels are multiplexed together. For example, the signals may be combined using time-domain multiplexing, optical wavelength division multiplexing, or a combination of the two. Suitable multiplexer / demultiplexers are provided at each end of extended interface 80. The illustrated embodiment provides a very

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high speed multi-port time domain multiplexer 100 operating at several gigabits per second and a compatible demultiplexer 101. Other embodiments use multiple links each comprising a fiber optic cable or other suitable transmission medium.

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[0058] Systems according to the invention may operate at power sufficient for operation as, for example, in cellular repeater stations. Other embodiments may operate at lower powers, for example in cellular hand sets. The systems may operate in any of various bands including VHF, UHF, SHF, EHF ( L, S, X, Ku, Ka, etc.). The system may operate with multiple narrow and wideband signals with different types of phase, frequency , amplitude and code domain modulations such as CDMA, W-CDMA, TDMA, FM, QPSK, multilevel QAM, QAM and so on.

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[0059] Some embodiments of the invention comprise a multi-band bandpass delta sigma modulator and a tunable output filter connected between amplifier 20 and antenna 24. Such embodiments may comprise a control system connected to switch the operation of the system between two or more different frequency bands. Such embodiments may be used, for example, in cellular telephone handsets. The frequency band may be automatically selected to access cellular connections on cellular and or selectable networks using more than one frequency band.

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[0060] Embodiments of the invention may support sampling of baseband transmit signals and direct up conversion to RF/microwave signals.

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[0061] Some embodiments of the invention comprise bandpass delta sigma modulators which have multiple decimation filters and are over sampled at high frequencies. Such embodiments may be capable of simultaneous multiple band operation. Such embodiments may be provided in base stations or handsets.

[0062] Where a component (e.g. an assembly, device, memory, etc.) is referred to herein, unless otherwise indicated, reference to that component (including a reference to a "means") should be interpreted as a reference to any component which performs the function of the described component (i.e. is functionally equivalent to the described component), including components which are not structurally equivalent to the disclosed structure which perform the function in the illustrated exemplary embodiments of the invention. Where a step in a method is referred to above, unless otherwise indicated, reference to that step should be interpreted as a reference to any step which achieves the same result as the step (i.e. is functionally equivalent to the described step), including steps which achieve a stated result in different ways from those disclosed in the illustrated exemplary embodiments of the invention.

[0063] As will be apparent to those skilled in the art in the light of the foregoing disclosure, many alterations and modifications are possible in the practice of this invention without departing from the spirit or scope thereof. For example:

- certain aspects of the invention could be implemented in software or hardware or a combination of software and hardware;

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- the various functional components illustrated in the drawings may be combined with one another in different ways.

Accordingly, the scope of the invention is to be construed in accordance with the substance defined by the following claims.